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Patent Abstracts of Japan

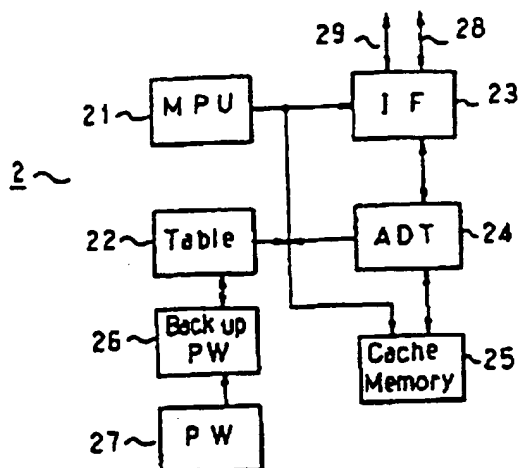
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AB. DATE : 05-02-1986 PAT: A 60179857
PATENTEE : FUJITSU KK
PATENT DATE: 13-09-1985

INVENTOR : SEKI KAZUHISA

INT.CL. : G06F12/08; G06F12/16

TITLE : CONTROL SYSTEM OF CACHE
DEVICE



ABSTRACT : PURPOSE: To return a cache device to a state immediately before the disconnection of a power supply and to drive instantaneously the device by transferring data existing in the cache memory at the OFF of the power supply from a disc to the memory at the rerise of the cache device.
CONSTITUTION: Data having high frequency are stored in the cache memory 25 and addresses on a disc device 3 in which these data exist are registered in a control table 22. If a power supply 7 is turned off under said status, a backup power supply is turned on by a disconnection signal to protect information in a control table 2. At the time turning on the power supply again, address information registered in the control table 2 is read out by a microprogram from an MPU21 and the disc device 3 is accessed by using a control interface 28, an interface circuit 23 and a transfer circuit 24 to restore the corresponding data in the cache memory 25.